Electronic Acknowledgement Receipt				
EFS ID:	2040130			
Application Number:	10796672			
International Application Number:				
Confirmation Number:	3015			
Title of Invention:	SEMICONDUCTOR DEVICE TEST PATTERNS AND RELATED METHODS FOR PRECISELY MEASURING LEAKAGE CURRENTS IN SEMICONDUCTOR CELL TRANSISTORS			
First Named Inventor/Applicant Name:	Young-pil Kim			
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1 Issue Fee Payment (PTO-85B)	Issue Fee Payment (PTO-95R)	5649 1276 IssueFee.pdf	204614	no	
	3049_1270_133de1 ee.pdi	59c52bc300001e7c4a71040b9922c611 ca6b7151	110		
Warnings:					
Information:	:				
2 Fee Worksheet (PTO-06)	Fee Worksheet (PTO-06)	fee-info.pdf	8356	no	2
	iee-iiio.pai	2xdb48613aab889e3a4d930b6b2a4b77 203662a2	1		
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.53) (b) lo issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.